

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Yoshiaki Mori, et al. Art Unit: 1795
Serial No.: 10/026,286 Examiner: Daborah Chacko Davis
Filed: December 20, 2001 Confirmation No.: 1796
Title: Pattern Forming Method and Apparatus Used for Semiconductor Device, Electric Circuit, Display Module, and Light Emitting Device

RESPONSE G

Sir:

In response to the Office Action dated June 16, 2008 (Paper No./Mail Date 20080609), please amend the above-identified application as follows: